

© Copyright SEK. Reproduction in any form without permission is prohibited.

Solceller – Datablad och produktinformation för skivor av kristallint kisel för tillverkning av solceller

*Solar wafers –
Data sheet and product information for crystalline silicon wafers for solar cell manufacturing*

Som svensk standard gäller europastandarden EN 50513:2009. Den svenska standarden innehåller den officiella engelska språkversionen av EN 50513:2009.

ICS 27.160

Denna standard är fastställd av SEK Svensk Elstandard,
som också kan lämna upplysningar om **sakinnehållet** i standarden.
Postadress: SEK, Box 1284, 164 29 KISTA
Telefon: 08 - 444 14 00. Telefax: 08 - 444 14 30
E-post: sek@elstandard.se. Internet: www.elstandard.se

Standarder underlättar utvecklingen och höjer elsäkerheten

Det finns många fördelar med att ha gemensamma tekniska regler för bl a säkerhet, prestanda, dokumentation, utförande och skötsel av elprodukter, elanläggningar och metoder. Genom att utforma sådana standarder blir säkerhetskraven tydliga och utvecklingskostnaderna rimliga samtidigt som marknadens acceptans för produkten eller tjänsten ökar.

Många standarder inom elområdet beskriver tekniska lösningar och metoder som åstadkommer den elsäkerhet som föreskrivs av svenska myndigheter och av EU.

SEK är Sveriges röst i standardiseringssarbetet inom elområdet

SEK Svensk Elstandard svarar för standardiseringen inom elområdet i Sverige och samordnar svensk medverkan i internationell och europeisk standardisering. SEK är en ideell organisation med frivilligt deltagande från svenska myndigheter, företag och organisationer som vill medverka till och påverka utformningen av tekniska regler inom elektrotekniken.

SEK samordnar svenska intressenters medverkan i SEKs tekniska kommittéer och stödjer svenska experters medverkan i internationella och europeiska projekt.

Stora delar av arbetet sker internationellt

Utdriften av standarder sker i allt väsentligt i internationellt och europeiskt samarbete. SEK är svensk nationalkommitté av International Electrotechnical Commission (IEC) och Comité Européen de Normalisation Electrotechnique (CENELEC).

Standardiseringssarbetet inom SEK är organiserat i referensgrupper bestående av ett antal tekniska kommittéer som speglar hur arbetet inom IEC och CENELEC är organiserat.

Arbetet i de tekniska kommittéerna är öppet för alla svenska organisationer, företag, institutioner, myndigheter och statliga verk. Den årliga avgiften för deltagandet och intäkter från försäljning finansierar SEKs standardiseringssverksamhet och medlemsavgift till IEC och CENELEC.

Var med och påverka!

Den som deltar i SEKs tekniska kommittéarbete har möjlighet att påverka framtidens standarder och får tidig tillgång till information och dokumentation om utvecklingen inom sitt teknikområde. Arbetet och kontakterna med kollegor, kunder och konkurrenter kan gynnsamt påverka enskilda företags affärsutveckling och bidrar till deltagarnas egen kompetensutveckling.

Du som vill dra nytta av dessa möjligheter är välkommen att kontakta SEKs kansli för mer information.

SEK Svensk Elstandard

Box 1284
164 29 Kista
Tel 08-444 14 00
www.elstandard.se

English version

**Solar wafers -
Data sheet and product information for crystalline silicon wafers
for solar cell manufacturing**

Tranches de silicium solaires -
Fiche technique et information produit
sur les tranches au silicium cristallin
pour la fabrication de cellules solaires

Solarscheiben -
Datenblattangaben und Produktinformation
für kristalline Silizium-Scheiben
zur Solarzellengerüstherstellung

This European Standard was approved by CENELEC on 2008-12-01. CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

Up-to-date lists and bibliographical references concerning such national standards may be obtained on application to the Central Secretariat or to any CENELEC member.

This European Standard exists in three official versions (English, French, German). A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the Central Secretariat has the same status as the official versions.

CENELEC members are the national electrotechnical committees of Austria, Belgium, Bulgaria, Cyprus, the Czech Republic, Denmark, Estonia, Finland, France, Germany, Greece, Hungary, Iceland, Ireland, Italy, Latvia, Lithuania, Luxembourg, Malta, the Netherlands, Norway, Poland, Portugal, Romania, Slovakia, Slovenia, Spain, Sweden, Switzerland and the United Kingdom.

CENELEC

European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

Central Secretariat: avenue Marnix 17, B - 1000 Brussels

Foreword

This European Standard was prepared by the Technical Committee CENELEC TC 82, Solar photovoltaic energy systems.

The text of the draft was submitted to the Unique Acceptance Procedure and was approved by CENELEC as EN 50513 on 2008-12-01.

The following dates were fixed:

- latest date by which the EN has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2009-12-01
 - latest date by which the national standards conflicting with the EN have to be withdrawn (dow) 2011-12-01
-

Contents

1	Scope	6
2	Normative references.....	6
3	Terms, definitions and acronyms.....	7
4	Crystallisation process.....	7
5	Product characteristics	7
5.1	Size	7
5.2	Electrical characteristics	8
5.3	Surface and edge characteristics.....	8
5.4	Chemical characteristics	9
5.5	Crystal characteristics	9
6	Packaging, marking and storage.....	9
6.1	Packaging	9
6.2	Marking	9
6.3	Storage.....	9
7	Major changes of product and processes	9
8	Wafer thickness	10
8.1	Scope	10
8.2	Normative references.....	10
8.3	Definitions	10
8.4	Units	10
8.5	Measuring equipment.....	11
8.6	Measurement	11
8.7	Evaluation	12
8.8	Test report	12
8.9	Precision of the procedure	12
9	Variations in thickness	13
9.1	Scope	13
9.2	Normative references.....	13
9.3	Definition	13
9.4	Units	13
9.5	Measuring equipment.....	13
9.6	Measurement	14
9.7	Evaluation	15
9.8	Test report	14
9.9	Precision of the procedure	15
10	Waviness and warping.....	15
10.1	Scope	15
10.2	Normative references.....	15
10.3	Definitions	15
10.4	Arrangements.....	16
10.5	Test report	16
11	Grooves and step type saw mark	16
11.1	Scope	16
11.2	Definitions	16
11.3	Units	17
11.4	Arrangements.....	17
11.5	Measuring devices	17

11.6 Measuring plan.....	18
11.7 Test report.....	20
12 Corrosion rate	20
12.1 Scope	20
12.2 Definitions	20
12.3 Implementation.....	20
12.4 Analysis.....	21
12.5 Test report.....	21
13 Determining carrier lifetime measured on as cut wafer	21
13.1 Scope	21
13.2 Determination of carrier lifetime	22
13.3 General measuring conditions	23
13.4 Analysis.....	23
13.5 Test report.....	24
14 Determining minority carrier bulk lifetime measured on passivated wafers (laboratory measurement).....	24
14.1 Scope	24
14.2 Determination of carrier lifetime	24
14.3 Analysis.....	26
14.4 Test report.....	26
15 Electrical resistivity of multi and mono crystalline semiconductor wafers.....	26
15.1 Scope	26
15.2 Normative references.....	26
15.3 Definition	26
15.4 Units	26
15.5 Measuring devices	27
15.6 Calibration	27
15.7 Sample size.....	27
15.8 Measurement of silicon wafers	27
15.9 Test report.....	28
16 Method for the measurement of substitutional atomic carbon and interstitial oxygen content in silicon used as solar material.....	28
16.1 Scope	28
16.2 Referenced documents (Normative references).....	28
16.3 Definitions	28
16.4 Units	28
16.5 Arrangements.....	28
16.6 Measurement	28
16.7 Test report.....	29
Annex A (informative) Geometric dimensions, surfaces and edge characteristics	30
Annex B (informative) Optional requirements	33
Bibliography.....	34

Figures

Figure 1 – Measurement plan for full-square and pseudo-square silicon wafers	12
Figure 2 – Sketch of waviness	15
Figure 3 – Sketch of warping.....	16
Figure 4 – Sketch of a groove on a crystalline silicon solar wafer	16
Figure 5 – Sketch of a step on a crystalline silicon solar wafer	17
Figure 6 – Measurement plan for full-square and pseudo-square silicon wafers	18
Figure 7 – Measurement plan for steps	19
Figure 8 – Measurement plan for grooves	19
Figure 9 – Measurement plan for carrier lifetime measurement	23
Figure A.1 – Rectangular wafer with flat chamfer	30
Figure A.2 – Square wafer with round chamfer.....	30
Figure A.3 – Error patterns for surface and edge characteristic	31
Figure A.4 – Edge swelling.....	32
Figure A.5 – Deviation from ideal edge	32

Table

Table 1 – Surface and edge characteristics	8
--	---

1 Scope

This document describes data sheet and product information for crystalline silicon (Si) – solar wafers and measurement methods for wafer properties.

The document intends to provide the minimum information required for an optimal use of crystalline silicon wafers in solar cell manufacturing. Clauses 5 to 7 describe the data sheet information with technical specifications of the silicon solar wafer with all essential characteristics.

The product information concerns packaging, labelling and storage, and implies the commitment to inform about major changes of the product and in the manufacturing process. This data is needed for the processing of silicon solar wafers to solar cells. Clauses 8 to 16 describe measurement methods for the characteristic properties specified in the data sheet.

2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

EN 50461, Solar cells – Datasheet information and product data for crystalline silicon solar cells

EN ISO/IEC 17025, General requirements for the competence of testing and calibration laboratories (ISO/IEC 17025)

DIN 5043-2, Radioactive luminescent pigments – Method of measurement and designation

DIN 50431, Testing of semiconductor materials – Measurement of the resistivity of silicon or germanium single crystals by means of the four probe/direct current method with collinear array

DIN 50432, Testing semi-conducting inorganic materials – Determining conduction type for silicon or germanium using a standard test or thermosonde probe (withdrawn)

DIN 50434, Testing of materials for semiconductor technology – Detection of crystal defects in mono-crystalline silicon using etching techniques on [111] and [100] surfaces (ASTM F 47)

DIN 50438-1, Testing of materials for semiconductor technology – Determination of impurity content in silicon by infrared absorption – Part 1: Oxygen

DIN 50438-2, Testing of materials for semiconductor technology – Determination of impurity content in silicon by infrared absorption – Part 2: Carbon

DIN 50441-1, Testing of materials for semiconductor technology – Determination of the geometric dimensions of semiconductor wafers – Part 1: Thickness and thickness variation

DIN 50441-5, Testing of materials for semiconductor technology – Determination of the geometric dimensions of semiconductor wafers – Part 5: Terms of shape and flatness deviation

DIN 4760, Form deviations – Concepts – Classification system

DIN 4768, Identification of roughness characteristics R_a , R_z , R_{max} with electrical testing machines – terms, conditions of measurement

DIN 879-1, Verification of geometrical parameters – Dial indicator for linear measurement – Part 1: With mechanical indication

DIN 4774, Measurement of wave depth with electrical contact stylus instruments