IEC 61191-1

Edition 3.0 2018-09

REDLINE VERSION

colour inside

Printed board assemblies –
Part 1: Generic specification – Requirements for soldered electrical and electronic assemblies using surface mount and related assembly technologies

INTERNATIONAL
ELECTROTECHNICAL
COMMISSION

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INTERNATIONAL ELECTROTECHNICAL COMMISSION

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PRINTED BOARD ASSEMBLIES –

Part 1: Generic specification – Requirements for soldered electrical and electronic assemblies using surface mount and related assembly technologies

FOREWORD

1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
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This Redline version provides you with a quick and easy way to compare all the changes between this standard and its previous edition. A vertical bar appears in the margin wherever a change has been made. Additions are in green text, deletions are in strikethrough red text.

International Standard IEC 61191-1 has been prepared by IEC technical committee 91: Electronics assembly technology.

This third edition cancels and replaces the second edition published in 2013. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

a) the requirements have been updated to be compliant with the acceptance criteria in IPC-A-610F;
b) the term "assembly drawing" has been changed to "assembly documentation" throughout;
c) references to IEC standards have been corrected;
d) Clause 9 was completely rewritten;
e) Annex B was removed because there are already procedures for circuit board assemblies.

The text of this International Standard is based on the following documents:

CDV	Report on voting
91/1481/CDV	91/1510/RVC

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts of IEC 61191 series, published under the general title *Printed board assemblies*, can be found in the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
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PRINTED BOARD ASSEMBLIES –

Part 1: Generic specification – Requirements for soldered electrical and electronic assemblies using surface mount and related assembly technologies

1 Scope

This part of IEC 61191 prescribes requirements for materials, methods and verification criteria for producing quality soldered interconnections and assemblies using surface mount and related assembly technologies. This part of IEC 61191 also includes recommendations for good manufacturing processes.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60068-2-20, *Environmental testing – Part 2-20: Tests – Test T: Test methods for solderability and resistance to soldering heat of devices with leads*

IEC 60068-2-58, *Environmental testing – Part 2-58: Tests – Test Td: Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)*

IEC 60194, *Printed board design, manufacture and assembly – Terms and definitions*

IEC 60721-3-1, *Classification of environmental conditions – Part 3: Classification of groups of environmental parameters and their severities – ~~Section 1:~~ Storage*

~~IEC 61188-1-1, *Printed boards and printed board assemblies – Design and use – Part 1-1: Generic requirements – Flatness considerations for electronic assemblies*~~

IEC 61189-1, *Test methods for electrical materials, interconnection structures and assemblies – Part 1: General test methods and methodology*

IEC 61189-3, *Test methods for electrical materials, printed boards and other interconnection structures and assemblies – Part 3: Test methods for interconnection structures (printed boards)*

IEC 61190-1-1, *Attachment materials for electronic assembly – Part 1-1: Requirements for soldering fluxes for high-quality interconnections in electronics assembly*

~~IEC 61190-1-2, *Attachment materials for electronic assembly – Part 1-2: Requirements for soldering pastes for high-quality interconnects in electronics assembly*~~

IEC 61190-1-3, *Attachment materials for electronic assembly – Part 1-3: Requirements for electronic grade solder alloys and fluxed and non-fluxed solid solders for electronic soldering applications*

IEC 61191-2, *Printed board assemblies – Part 2: Sectional specification – Requirements for surface mount soldered assemblies*

IEC 61191-3, *Printed board assemblies – Part 3: Sectional specification – Requirements for through-hole mount soldered assemblies*

IEC 61191-4, *Printed board assemblies – Part 4: Sectional specification – Requirements for terminal soldered assemblies*

IEC 61249-8-8, *Materials for interconnection structures – Part 8: Sectional specification set for non-conductive films and coatings – Section 8: Temporary polymer coatings*

IEC 61340-5-1, *Electrostatics – Part 5-1: Protection of electronic devices from electrostatic phenomena – General requirements*

IEC/TR 61340-5-2, *Electrostatics – Part 5-2: Protection of electronic devices from electrostatic phenomena – User guide*

IEC 61760-2, *Surface mounting technology – Part 2: Transportation and storage conditions of surface mounting devices (SMD) – Application guide*

ISO 9001:2008, *Quality management systems – Requirements*

IPC-A-610~~E:2010~~, *Acceptability of Electronic Assemblies*

3 Terms and definitions

For the purposes of this document, the terms and definitions given in IEC 60194 and the following apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

3.1
bow
deviation from flatness of a board characterized by a roughly cylindrical or spherical curvature so that, if the product is rectangular, its four corners are in the same plane

3.2
manufacturer
assembler
individual or company responsible for the procurement of materials and components, as well as all assembly process and verification operations necessary to ensure full compliance of assemblies with this document

3.3
objective evidence
documentation agreed to between the user and the manufacturer

Note 1 to entry: The documentation can be in the form of a hard copy, computer data, computer algorithms, video or other media.

3.4
process indicator
detectable anomaly, other than a defect, that is reflective of material, equipment, personnel, process and/or workmanship variation

3.5
proficiency
capability to perform tasks in accordance with the requirements and verification procedures detailed in this document

3.6
shadowing
phenomenon where parts create a shadow of leads, lands, or other parts, which obstruct heating at reflow soldering or spreading solder at flow soldering

3.7
supplier
individual or company responsible for assuring, to the manufacturer (assembler), full compliance of components and base materials with the requirements and verification procedures of this document

Note 1 to entry: Components include electronic, electromechanical, mechanical components, printed boards, etc.

Note 2 to entry: Base materials include solder, flux, cleaning agents, etc.

3.8
twist
deviation of a rectangular sheet, panel or printed board that occurs parallel to a diagonal across its surface, so that one of the corners of the sheet is not in the plane that contains the other three corners

3.9
user
procuring authority
individual, company or agency responsible for the procurement of electrical/electronic hardware, and having the authority to define the class of equipment and any variation or restrictions to the requirements of this document

EXAMPLE The originator/custodian of the contract detailing these requirements.

IEC 61191-1

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INTERNATIONAL STANDARD

NORME INTERNATIONALE

Printed board assemblies –
Part 1: Generic specification – Requirements for soldered electrical and electronic assemblies using surface mount and related assembly technologies

Ensembles de cartes imprimées –
Partie 1: Spécification générique – Exigences relatives aux ensembles électriques et électroniques brasés utilisant les techniques de montage en surface et associées

IEC 61191-1:2018-09(en-fr)

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IEC 60068-2-58, *Environmental testing – Part 2-58: Tests – Test Td: Test methods for solderability, resistance to dissolution of metallization and to soldering heat of surface mounting devices (SMD)*

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IEC 61191-4, *Printed board assemblies – Part 4: Sectional specification – Requirements for terminal soldered assemblies*

IEC 61249-8-8, *Materials for interconnection structures – Part 8: Sectional specification set for non-conductive films and coatings – Section 8: Temporary polymer coatings*

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ISO 9001:2008, *Quality management systems – Requirements*

IPC-A-610*, Acceptability of Electronic Assemblies*

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COMMISSION ELECTROTECHNIQUE INTERNATIONALE

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ENSEMBLES DE CARTES IMPRIMÉES –

Partie 1: Spécification générique – Exigences relatives aux ensembles électriques et électroniques brasés utilisant les techniques de montage en surface et associées

AVANT-PROPOS

1) La Commission Electrotechnique Internationale (IEC) est une organisation mondiale de normalisation composée de l'ensemble des comités électrotechniques nationaux (Comités nationaux de l'IEC). L'IEC a pour objet de favoriser la coopération internationale pour toutes les questions de normalisation dans les domaines de l'électricité et de l'électronique. A cet effet, l'IEC – entre autres activités – publie des Normes internationales, des Spécifications techniques, des Rapports techniques, des Spécifications accessibles au public (PAS) et des Guides (ci-après dénommés "Publication(s) de l'IEC"). Leur élaboration est confiée à des comités d'études, aux travaux desquels tout Comité national intéressé par le sujet traité peut participer. Les organisations internationales, gouvernementales et non gouvernementales, en liaison avec l'IEC, participent également aux travaux. L'IEC collabore étroitement avec l'Organisation Internationale de Normalisation (ISO), selon des conditions fixées par accord entre les deux organisations.
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La Norme internationale IEC 61191-1 a été établie par le comité d'études 91 de l'IEC: Techniques d'assemblage des composants électroniques.

Cette troisième édition annule et remplace la deuxième édition parue en 2013. Cette édition constitue une révision technique.

Cette édition inclut les modifications techniques majeures suivantes par rapport à l'édition précédente:

a) les exigences ont été mises à jour pour être conformes aux critères d'acceptation de l'IPC-A-610F;
b) le terme "dessin d'assemblage" a été remplacé partout par "document d'assemblage";
c) les références aux normes IEC ont été corrigées;

d) l'Article 9 a été entièrement réécrit;

e) l'Annexe B a été retirée car des procédures d'assemblages de cartes à circuits existent déjà.

Le texte de cette Norme internationale est issu des documents suivants:

CDV	Rapport de vote
91/1481/CDV	91/1510/RVC

Le rapport de vote indiqué dans le tableau ci-dessus donne toute information sur le vote ayant abouti à l'approbation de cette Norme internationale.

Ce document a été rédigé selon les Directives ISO/IEC, Partie 2.

Une liste de toutes les parties de la série IEC 61191, publiées sous le titre général *Ensembles de cartes imprimées*, peut être consultée sur le site web de l'IEC.

Le comité a décidé que le contenu de ce document ne sera pas modifié avant la date de stabilité indiquée sur le site web de l'IEC sous "http://webstore.iec.ch" dans les données relatives au document recherché. A cette date, le document sera

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ENSEMBLES DE CARTES IMPRIMÉES –

Partie 1: Spécification générique – Exigences relatives aux ensembles électriques et électroniques brasés utilisant les techniques de montage en surface et associées

1 Domaine d'application

La présente partie de l'IEC 61191 établit les exigences relatives aux matériaux, méthodes et critères de vérification utilisés dans le cadre de la production d'interconnexions et d'ensembles brasés de qualité faisant appel à la technique de montage en surface ainsi qu'à des techniques d'assemblage associées. La présente partie de l'IEC 61191 comprend également des recommandations concernant la qualité des processus de fabrication.

2 Références normatives

Les documents suivants cités dans le texte constituent, pour tout ou partie de leur contenu, des exigences du présent document. Pour les références datées, seule l'édition citée s'applique. Pour les références non datées, la dernière édition du document de référence s'applique (y compris les éventuels amendements).

IEC 60068-2-20, *Essais d'environnement – Partie 2-20: Essais – Essai T: Méthodes d'essai de la brasabilité et de la résistance à la chaleur de brasage des dispositifs à broches*

IEC 60068-2-58, *Essais d'environnement – Partie 2-58: Essais – Essai Td: Méthodes d'essai de la soudabilité, de la résistance de la métallisation à la dissolution et de la résistance à la chaleur de brasage des composants pour montage en surface (CMS)*

IEC 60194, *Conception, fabrication et assemblage des cartes imprimées – Termes et définitions*

IEC 60721-3-1, *Classification des conditions d'environnement – Partie 3: Classification des groupements des agents d'environnement et de leurs sévérités – Stockage*

IEC 61189-1, *Méthodes d'essai pour les matériaux électriques, les structures d'interconnexion et les ensembles – Partie 1: Méthodes d'essai générales et méthodologie*

IEC 61189-3, *Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles – Partie 3: Méthodes d'essai des structures d'interconnexion (cartes imprimées)*

IEC 61190-1-1, *Matériaux de fixation pour les assemblages électroniques – Partie 1-1: Exigences relatives aux flux de brasage pour les interconnexions de haute qualité dans les assemblages de composants électroniques*

IEC 61190-1-3, *Matériaux de fixation pour les assemblages électroniques – Partie 1-3: Exigences relatives aux alliages à braser de catégorie électronique et brasure solide fluxée et non-fluxée pour les applications de brasage électronique*

IEC 61191-2, *Ensembles de cartes imprimées – Partie 2: Spécification intermédiaire – Exigences relatives à l'assemblage par brasage pour montage en surface*

IEC 61191-3, *Ensembles de cartes imprimées – Partie 3: Spécification intermédiaire – Exigences relatives à l'assemblage par brasage de trous traversants*

IEC 61191-4, *Ensembles de cartes imprimées – Partie 4: Spécification intermédiaire – Exigences relatives à l'assemblage de bornes par brasage*

IEC 61249-8-8, *Matériaux pour les structures d'interconnexion – Partie 8: Collection de spécifications intermédiaires pour les films et revêtements non conducteurs – Section 8: Revêtements amovibles de polymères*

IEC 61340-5-1, *Electrostatique – Partie 5-1: Protection des dispositifs électroniques contre les phénomènes électrostatiques – Exigences générales*

IEC/TR 61340-5-2, *Electrostatique – Partie 5-2: Protection des dispositifs électroniques contre les phénomènes électrostatiques – Guide d'utilisation*

IEC 61760-2, *Technique du montage en surface – Partie 2: Transport et stockage des composants pour montage en surface (CMS) – Guide d'application*

ISO 9001:2008, *Systèmes de management de la qualité – Exigences*

IPC-A-610*, Acceptabilité des assemblages électroniques*