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Railway applications – Systematic allocation of safety integrity requirements

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CENELEC

European Committee for Electrotechnical Standardization Comité Européen de Normalisation Electrotechnique Europäisches Komitee für Elektrotechnische Normung

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Foreword

This Technical Report was prepared by SC 9XA, Communication, signalling and processing systems, of Technical Committee CENELEC TC 9X, Electrical and electronic applications for railways.

The text of the draft was circulated for vote in accordance with the Internal Regulations, Part 2, Subclause 11.4.3.3 and was approved by CENELEC as CLC/TR 50451 on 2006-02-18.

This Technical Report supersedes R009-004:2001.

- 3 -

Executive summary		
Intr	roduction	
1	Scope	
2	References	
	2.1 Normative references	
	2.2 Informative references	
3	Definitions	
4	Symbols and abbreviations	
5	Safety Integrity Levels allocation framework	
	5.1 Prerequisites	
	5.2 Overview of the methodology	
	5.3 Definition of Safety Integrity Levels	
	5.4 Qualitative vs quantitative methods	
	5.4.1 Qualitative assessment	
	5.4.2 Quantitative assessment	
	5.5 EN 50126-1 lifecycle context	
6	System definition	
7	Hazard identification	
	7.1 General principles	
	7.2 Empirical hazard identification methods	
	7.3 Creative hazard identification methods	
	7.4 Hazard ranking	
	7.5 Existing hazard lists	
8	Risk analysis	
	8.1 Risk tolerability	
	8.2 Determination of Tolerable Hazard Rate	
	8.2.1 Qualitative risk analysis	
	8.2.2 Quantitative risk analysis	
	8.2.3 GAMAB and similar approaches	
	8.2.4 The MEM approach	
	8.2.5 Other approaches	
9	System design analysis	
0	9.1 Apportionment of safety integrity requirements to functions	
	9.1.1 Physical independence	
	9.1.2 Functional independence	
	9.1.3 Process independence	
	9.2 Use of SIL tables	
	9.3 Identification and treatment of new hazards arising from design	
	c c	
	9.4 Determination of function and subsystem SIL	
	9.5 Determination of safety integrity requirements for system elements	
Anı	nex A Single-line signalling system example	52
Anı	nex B Level crossing example	67
Anı	nex C Comparison of demand and continuous mode	77
Anı	nex D Frequently asked questions	

Executive summary

This Technical Report presents a systematic methodology to determine safety integrity requirements for railway signalling equipment, taking into account the operational environment and the architectural design of the signalling system.

At the heart of this approach is a well defined interface between the operational environment and the signalling system. From the safety point of view this interface is defined by a list of hazards and tolerable hazard rates associated with the system. It should be noted that the purpose of this approach is not to limit co-operation between suppliers and railway authorities but to clarify responsibilities and interfaces.

It is the task (summarized by the term Risk Analysis) of the Railway Authority

- to define the requirements of the railway system (independent of the technical realisation),
- to identify the hazards relevant to the system,
- to derive the tolerable hazard rates, and
- to ensure that the resulting risk is tolerable (with respect to the appropriate risk tolerability criteria).



Figure 0.1 - Global process overview

The only requirement is that the tolerable hazard rates must be derived taking into account the risk tolerability criteria. Risk tolerability criteria are not defined by this Technical Report, but depend on national or European legislative requirements.

Among the risk analysis methods two are proposed in order to estimate the individual risk explicitly, one more qualitative, the other more quantitative. Other methods, similar to the GAMAB principle, do not explicitly determine the resulting risks, but derive the tolerable hazard rates from comparison with the performance of existing systems, either by statistical or analytical methods. Alternative qualitative approaches are acceptable, if as a result they define a list of hazards and corresponding THR. The specification of the system requirements comprising performance and safety (THR) terminates the Railway Authority's task.



Figure 0.2 - Example Risk Analysis process

The supplier's task (summarized by the term System Design Analysis) comprises

- definition of the system architecture,
- analysis of the causes leading to each hazard,
- determination of the safety integrity requirements (SIL and hazard rates) for the subsystems,
- determination of the reliability requirements for the equipment.

Causal analysis constitutes two key stages. In the first phase the tolerable hazard rate for each hazard is apportioned to a functional level. Safety Integrity Levels (SIL) are defined at this functional level for the subsystems implementing the functionality. The hazard rate for a subsystem is then translated to a SIL using the SIL table.

During the second phase the hazard rates for subsystems are further apportioned leading to failure rates for the equipment, but at this physical implementation level the SIL remains unchanged. Consequently also the software SIL defined by EN 50128 would be the same as the subsystem SIL but for the exceptions described in EN 50128.

The apportionment process may be performed by any method which allows a suitable representation of the combination logic, e.g. reliability block diagrams, fault trees, binary decision diagrams, Markov models etc. In any case particular care must be taken when independence of items is required. While in the first phase of the causal analysis functional independence is required, physical independence is sufficient in the second phase. Assumptions made in the causal analysis must be checked and may lead to safety-relevant application rules for the implementation.



Figure 0.3 - Example System Design Analysis process

Both, the risk analysis and the system design analysis, have to be approved by the Railway Safety Authority.

However whilst the risk analysis may be carried out once at the railway level, the system design analysis must be performed for every new architecture. It is prudent to review the risk analysis and system design analysis when safety related changes are introduced.

Introduction

Historically the interoperability of European railways was not only hindered by incompatible technology but also by different approaches towards safety. The common European market is the main driving force behind the harmonisation of the different safety cultures. In a joint pan-European effort comprehensive safety standards have been established for railway signalling by the European Electrotechnical Standardisation Committee CENELEC:

- EN 50126-1, Railway applications The specification and demonstration of Reliability, Availability, Maintainability and Safety (RAMS) Part 1: Basic requirements and generic process
- EN 50128, Railway applications Communications, signalling and processing systems Software for railway control and protection systems
- EN 50129, Railway applications Communication, signalling and processing systems Safety related electronic systems for signalling

These CENELEC standards assume that safety relies both on adequate measures to prevent or tolerate faults (as safeguards against systematic failure) and on adequate measures to control random failures. Measures against both causes of failure should be balanced in order to achieve the optimum safety performance of a system. To achieve this the concept of Safety Integrity Levels (SIL) is used. SILs are used as a means of creating balance between measures to prevent systematic and random failures, as it is agreed within CENELEC that it is not feasible to quantify systematic integrity.

A shortcoming of the CENELEC standards as of today is (similar as in other related standards like IEC 61508¹⁾ [IEC] or ISA S84.01 [ISA]) that while the guidance on how to fulfil a particular SIL is quite comprehensive the process and rules to derive SILs for system elements from system safety targets or the tolerable system risk are not adequately covered. A general convincing solution to this problem is still an open research problem, see [LM][ZD][YB2][GAM] for some divergent examples. However in order to achieve cross-acceptance of safety cases and products for railway signalling applications it is necessary to fill the gap.

This has been realized by SC 9XA in 1997 and consequently a working group has been set up in March 1998 in order to find a joint harmonized approach at least for railway signalling applications. This work resulted in the publication of R009-004:2001, which is presently being converted into CLC/TR 50451.

Although the major driving forces behind this work were novel signalling applications which are required to be interoperable throughout Europe, the scope and applicability of the approach presented in this Technical Report should not be limited to signalling or interoperable applications.

¹⁾ IEC 61508 series has been harmonized as EN 61508 series "Functional safety of electrical/electronic/programmable electronic safety-related systems"

1 Scope

The scope of this Technical Report is to define a method to determine the required Safety Integrity Level of railway signalling equipment taking in consideration

- the operational conditions of the railway, and
- the architecture of the signalling system.

The following picture may be used in order to detail more precisely the scope of this Technical Report:



Figure 1.1 - Scope of WG A10

From a mechanistic point of view the task of this Technical Report is to define a method of calculation, which determines the integrity requirements (qualitatively and quantitatively) from the inputs stated above.

2 References

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

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